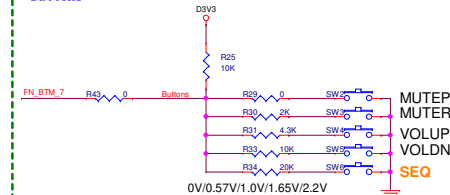


[illegible]

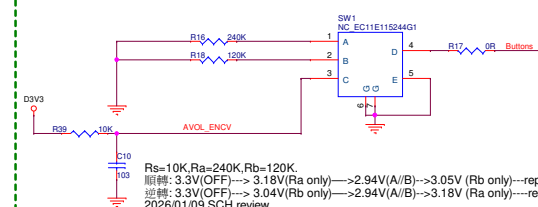
R14 for HiD1/HiD2/HiD3 key

The circuit diagram shows the D3V3 pin connected to the VR pin via a voltage divider network. The voltage divider consists of two resistors: R28 (1K) and VR1 (B50K). A capacitor C16 (104) is connected between the AVR VR pin and ground. The output of the voltage divider is labeled "AVOL VR".

Buttons



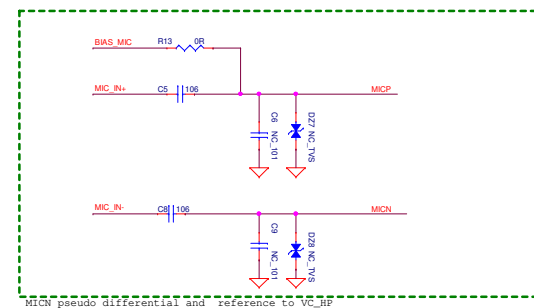
Encoder Control Volume



Rs=10K,Ra=240K,Rb=120K.
 正轉: 3.3V(OFF)---> 3.18V(Ra only)--->2.94V(A/B)-->3.05V (Rb only)---repeat
 逆轉: 3.3V(OFF)---> 3.04V(Rb only)--->2.94V(A/B)-->3.18V (Ra only)---repeat
 2026/01/09 SCH review
 Encoder pressed: pins D and E are shorted;
 behavior is the same as MUTE

HP VC Mode

2026/01/09 SCH review
 J1 unused pin: NC to avoid VC_HP short to GND.



MICN pseudo differential and reference to VC HP

