

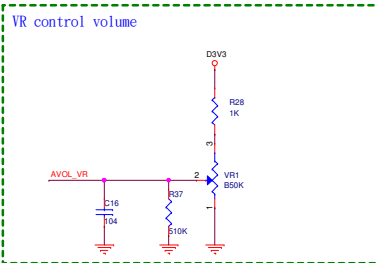
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Date		Rev.	Notice					Create by	
2026/01/12		V100	First Version Schematic					Marconi	

CM3109							
PB0		PB1	PB2	PB6		PB7	
LED_TEAMS	ENCV 或 VR	LED_MIC	LED_HOOK	Depop Control	MIC_IN-	OFF_HOOK	電阻1
						MIC_MUTE	電阻2
						VOLUP	電阻3
						VOLDN	電阻4
						TEAMS	電阻5
							電阻6
							電阻7
Config擇一							
SHARE PIN: PB0 & PB6							

C-MEDIA ELECTRONICS Inc. 6F., 100, Sec. 4, Civil Boulevard, Taipei, Taiwan, R.O.C.			
Title CMA208_CM3109_DemoBoard_Schematic_V100			
Size	Document Number		Rev
	RELEASE NOTE		V100
Date:	Friday, January 16, 2026	Sheet	1 of 2
2	1	1	

[illegible]

2026/01/09 SCH review
Not supports HID key

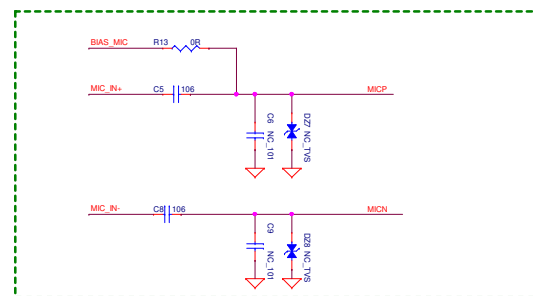


2026/01/09 SCH review
C9 changed from 105 to 104

[illegible]

HP VC Mode

The schematic diagram illustrates the HP VC Mode circuit. It features two input lines, HP_L and HP_R, each with a series resistor (R3, R6) and a series inductor (L2, L3). These are followed by a parallel combination of a capacitor (C1, C2) and a diode (D1, D2) connected to ground. The output lines are HPL, HPR, VC_HP, and MCP. A connector J2 is shown with pins 1 through 8, with pins 1, 2, 3, and 6 connected to the output lines.



MICN pseudo differential and reference to VC_HP

